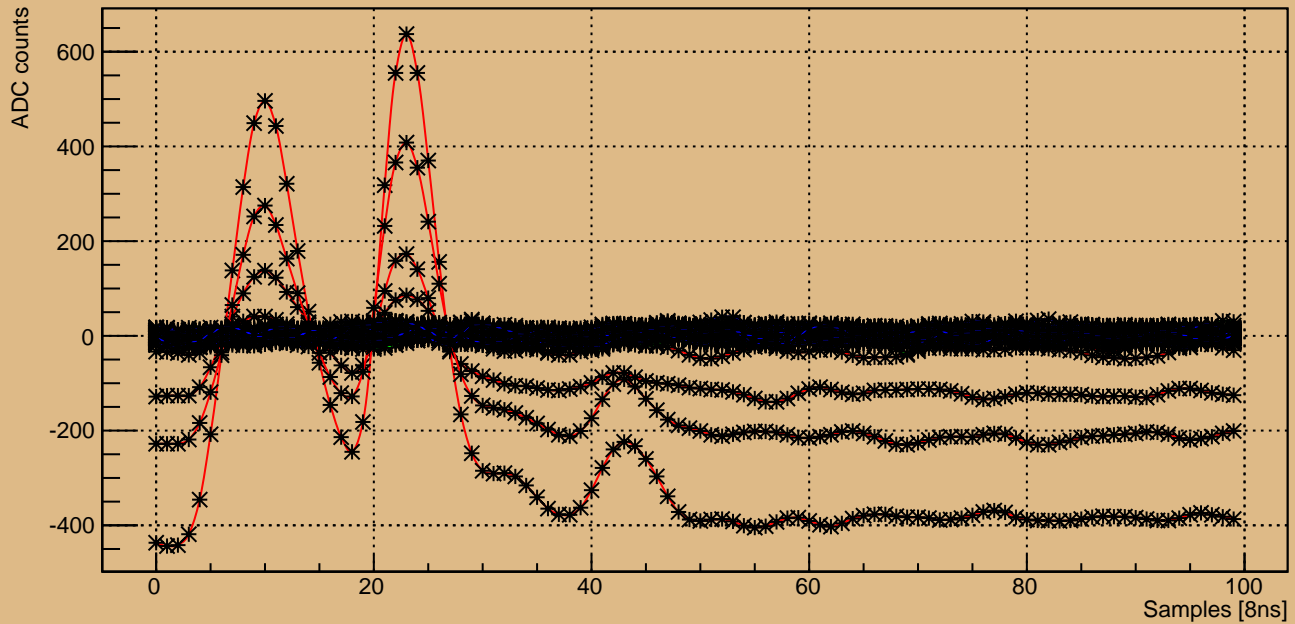
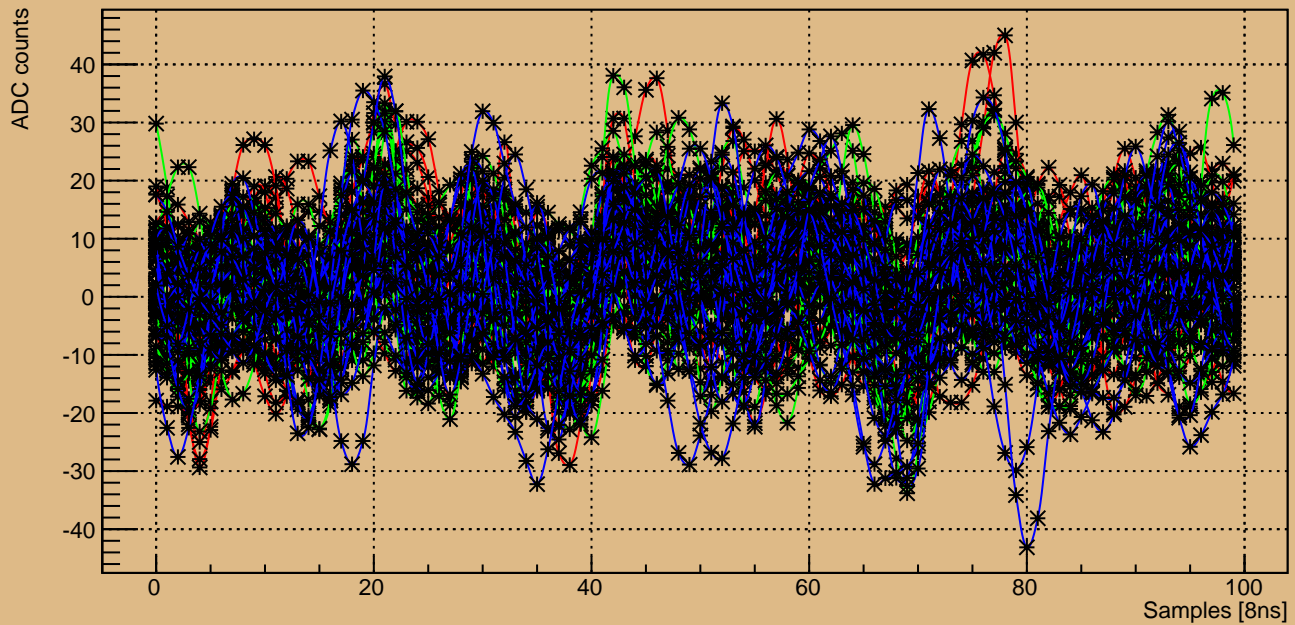


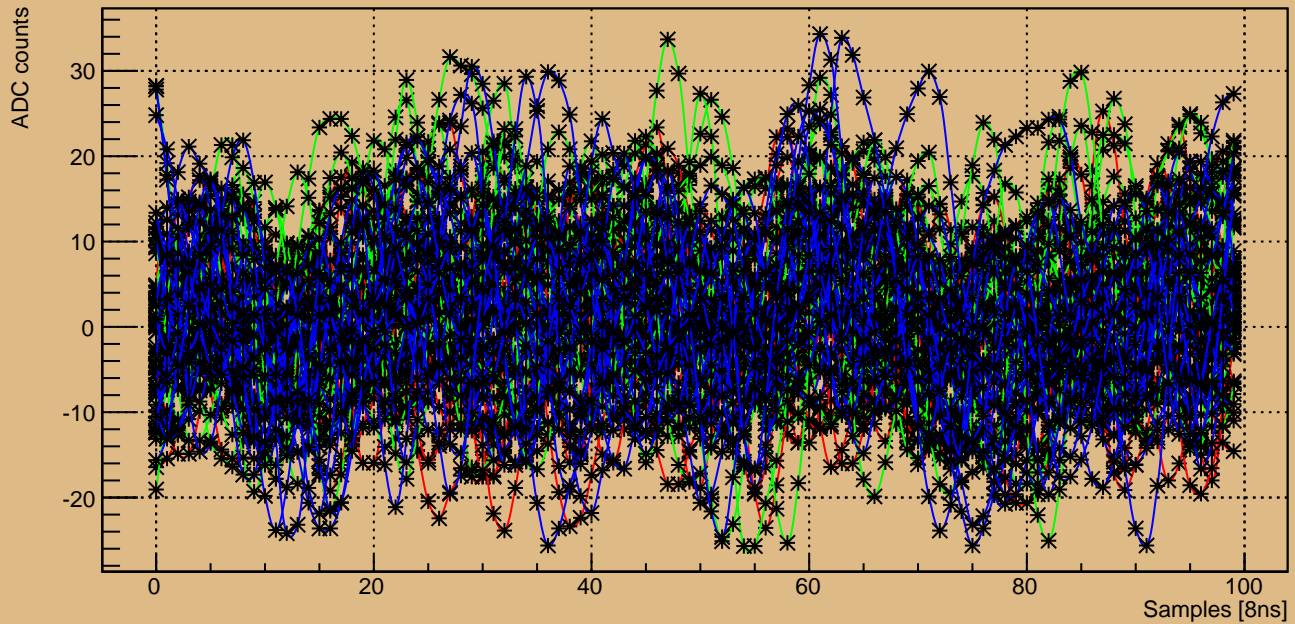
ADC125 Crate 1 ADC 0 raw data 72 channels



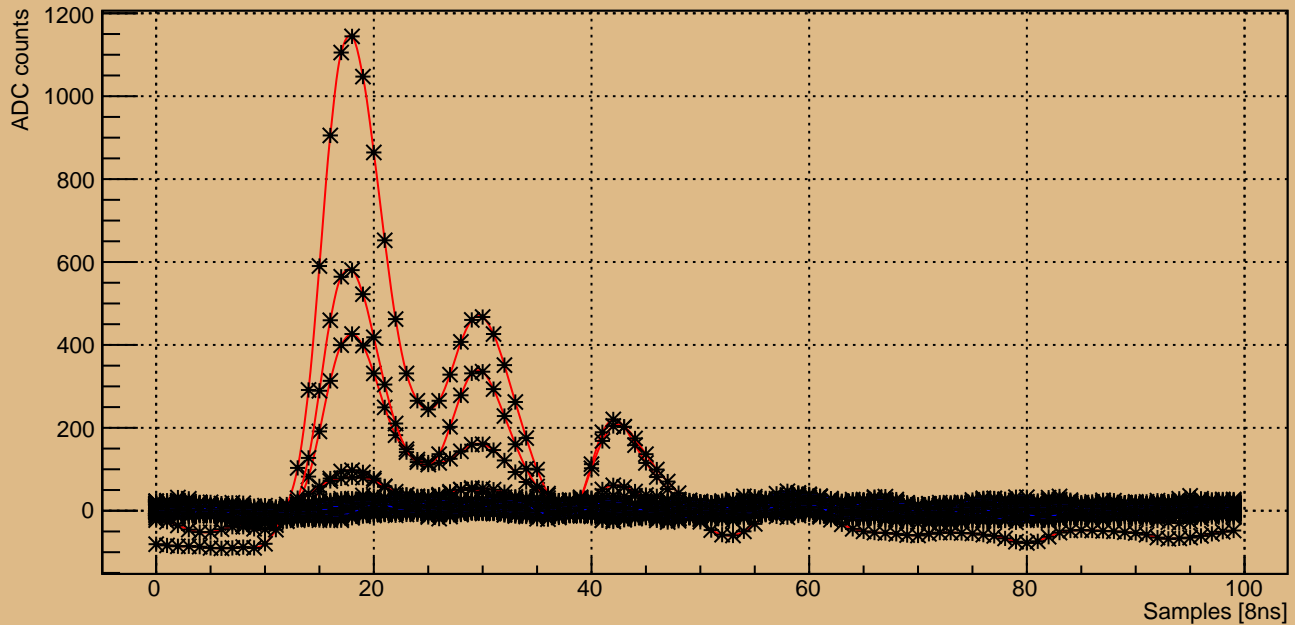
ADC125 Crate 1 ADC 1 raw data 72 channels



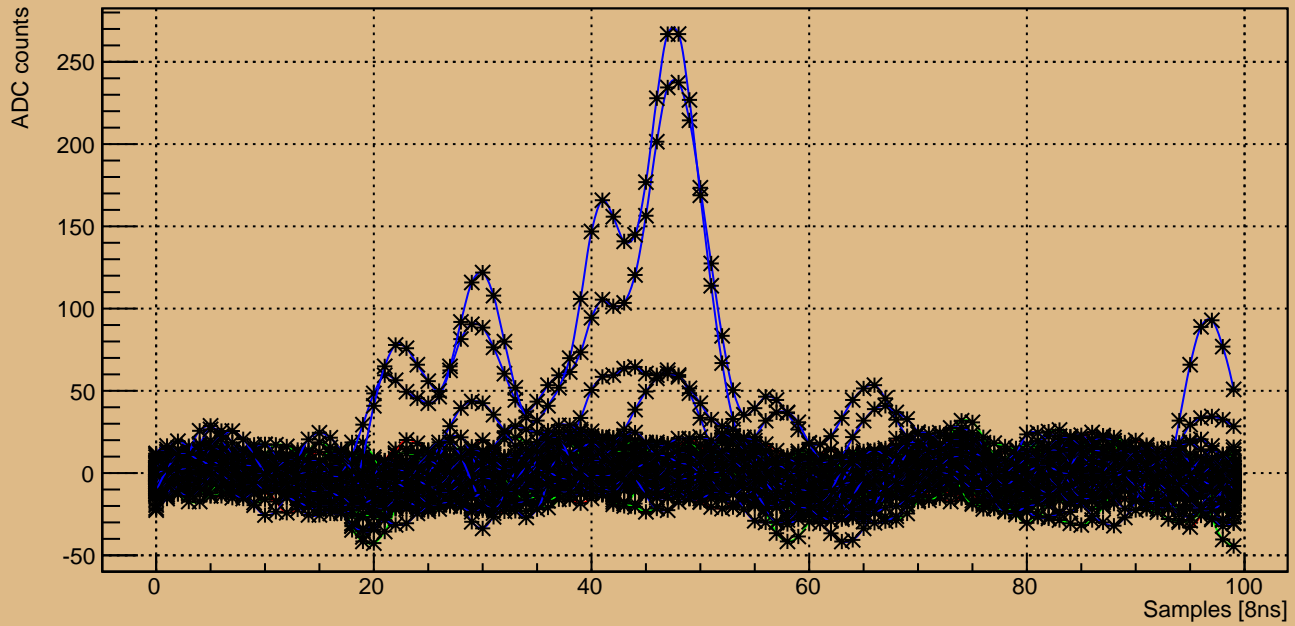
ADC125 Crate 1 ADC 0 raw data 72 channels



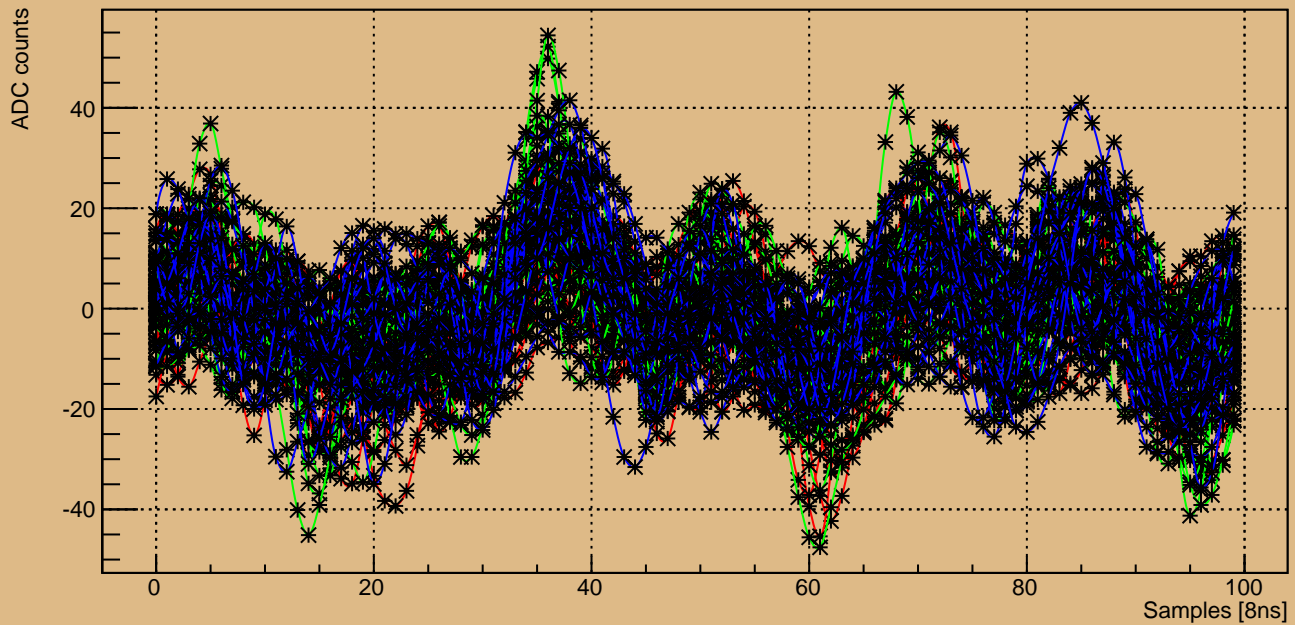
ADC125 Crate 1 ADC 1 raw data 72 channels



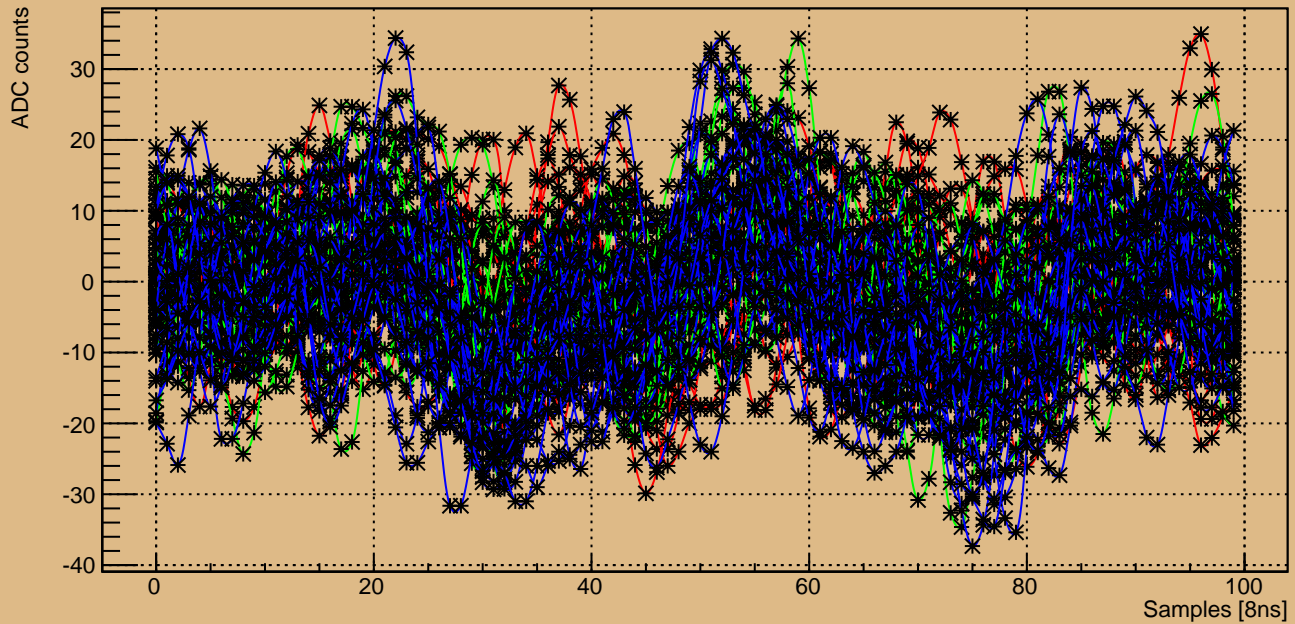
ADC125 Crate 1 ADC 0 raw data 72 channels



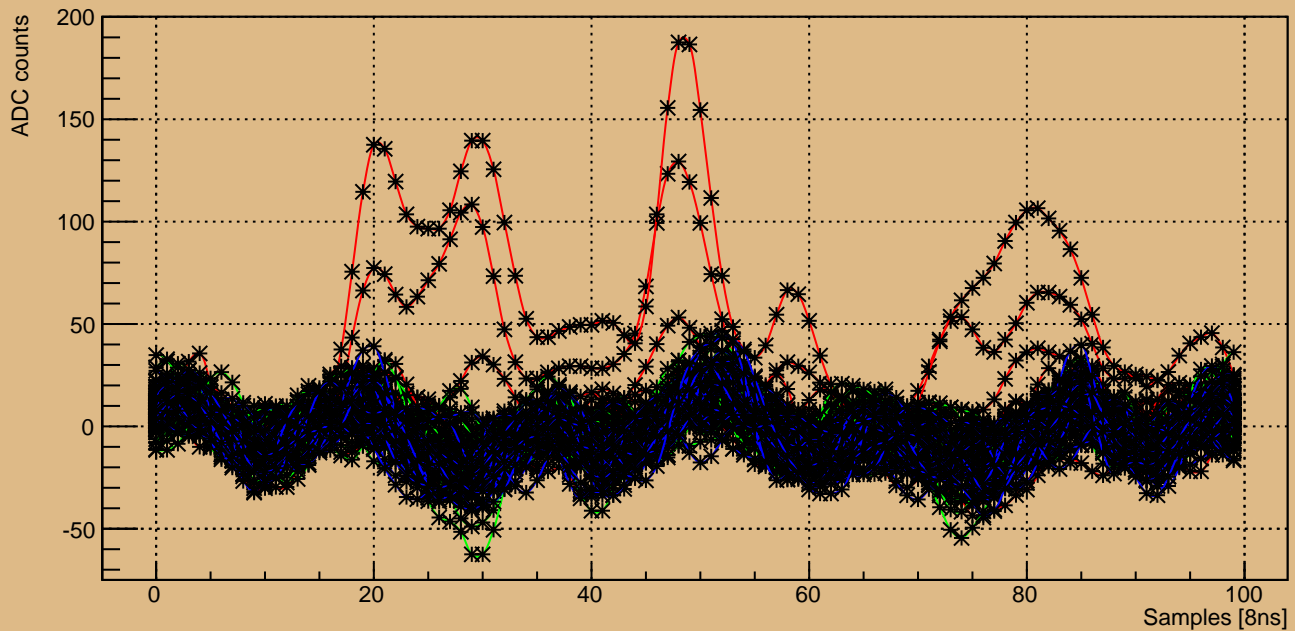
ADC125 Crate 1 ADC 1 raw data 72 channels



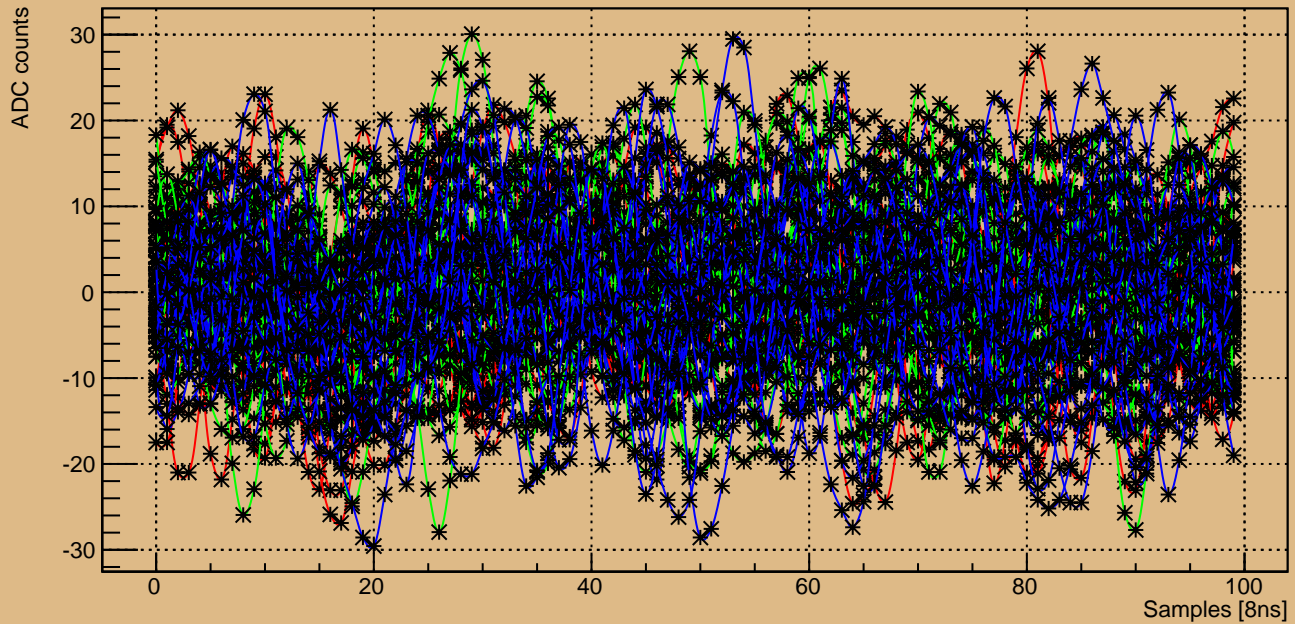
ADC125 Crate 1 ADC 0 raw data 72 channels



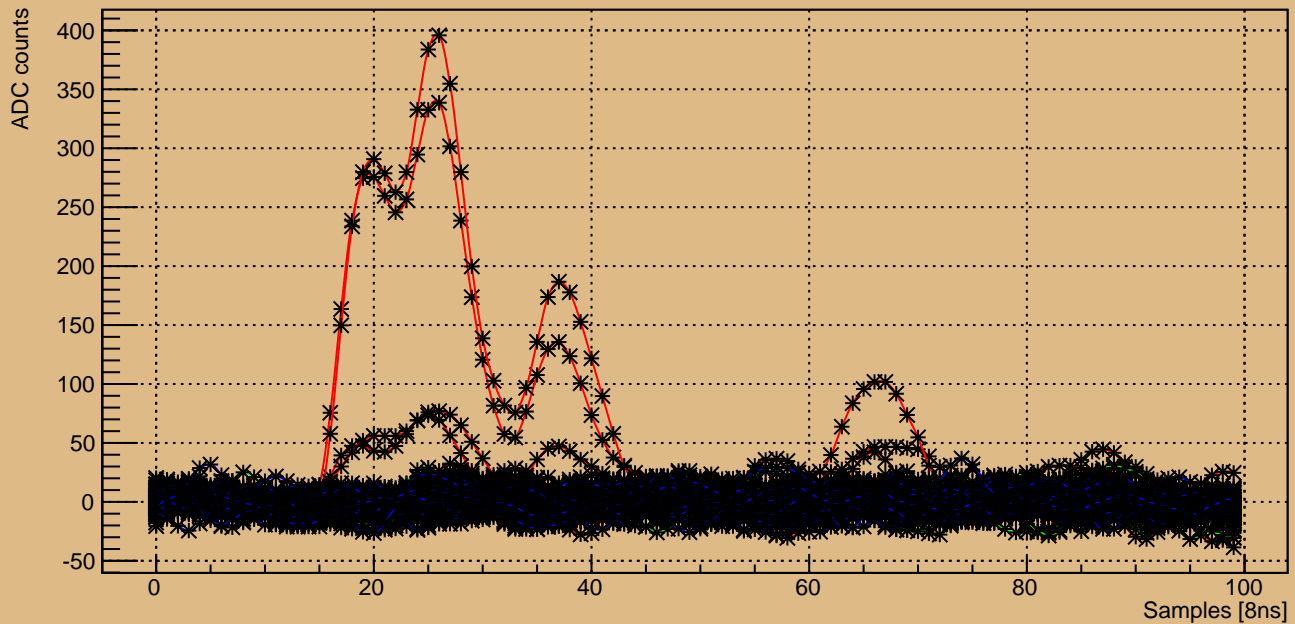
ADC125 Crate 1 ADC 1 raw data 72 channels



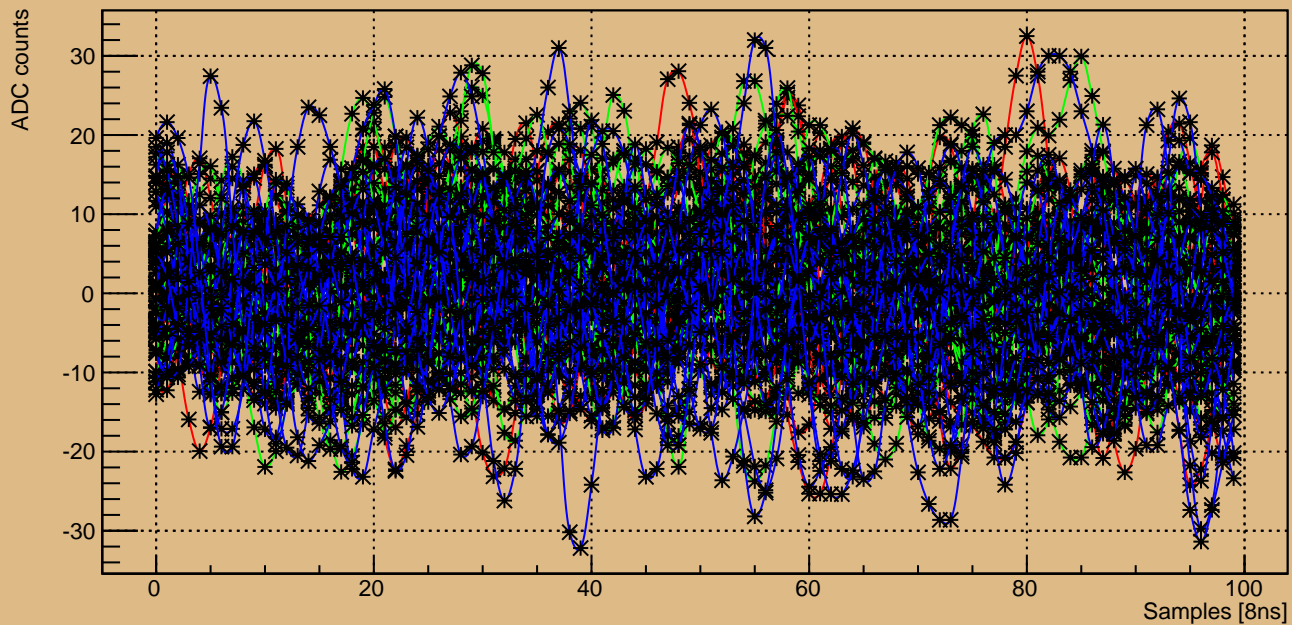
ADC125 Crate 1 ADC 0 raw data 72 channels



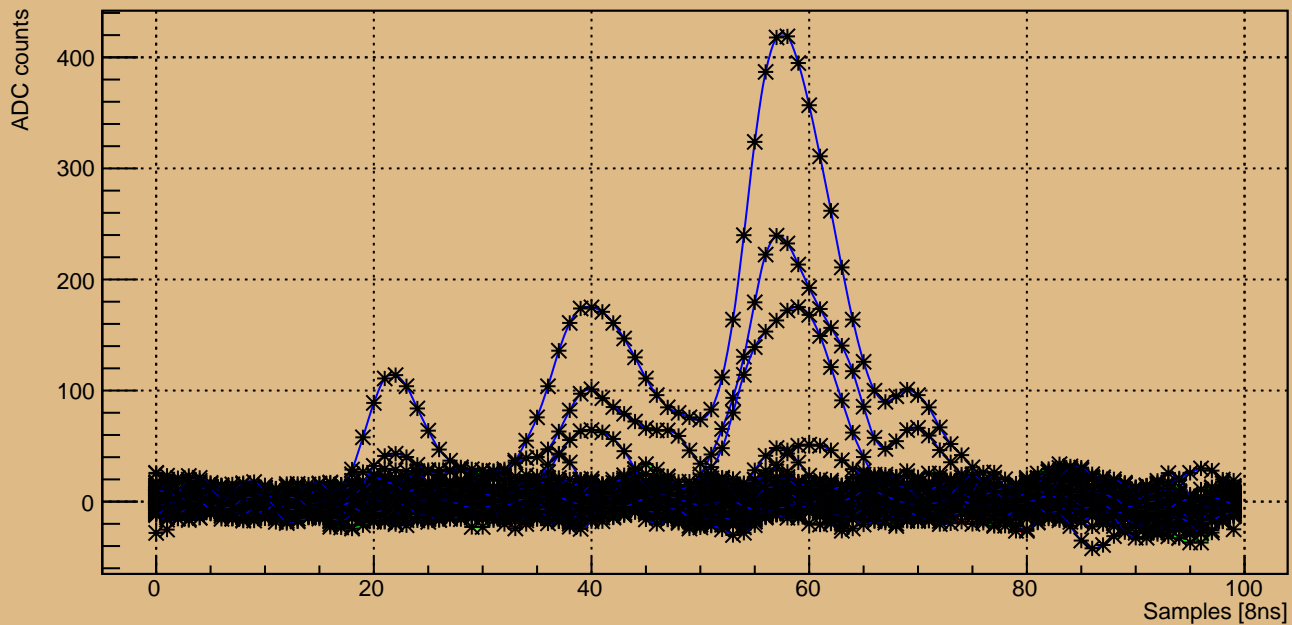
ADC125 Crate 1 ADC 1 raw data 72 channels



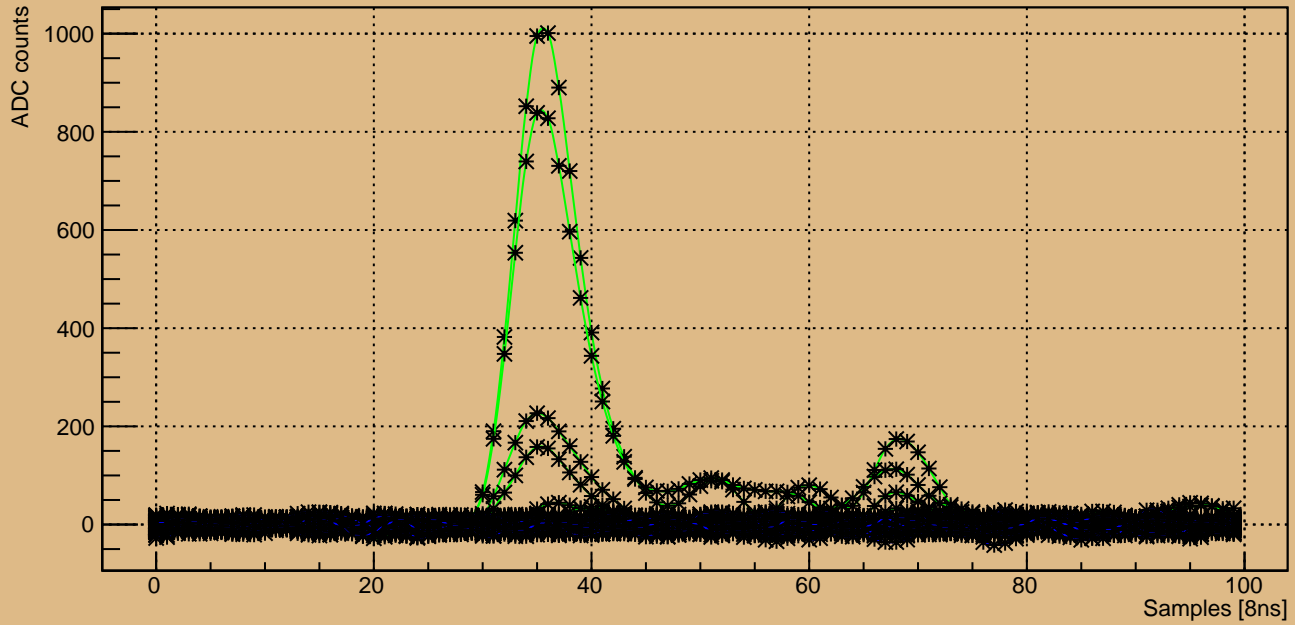
ADC125 Crate 1 ADC 0 raw data 72 channels



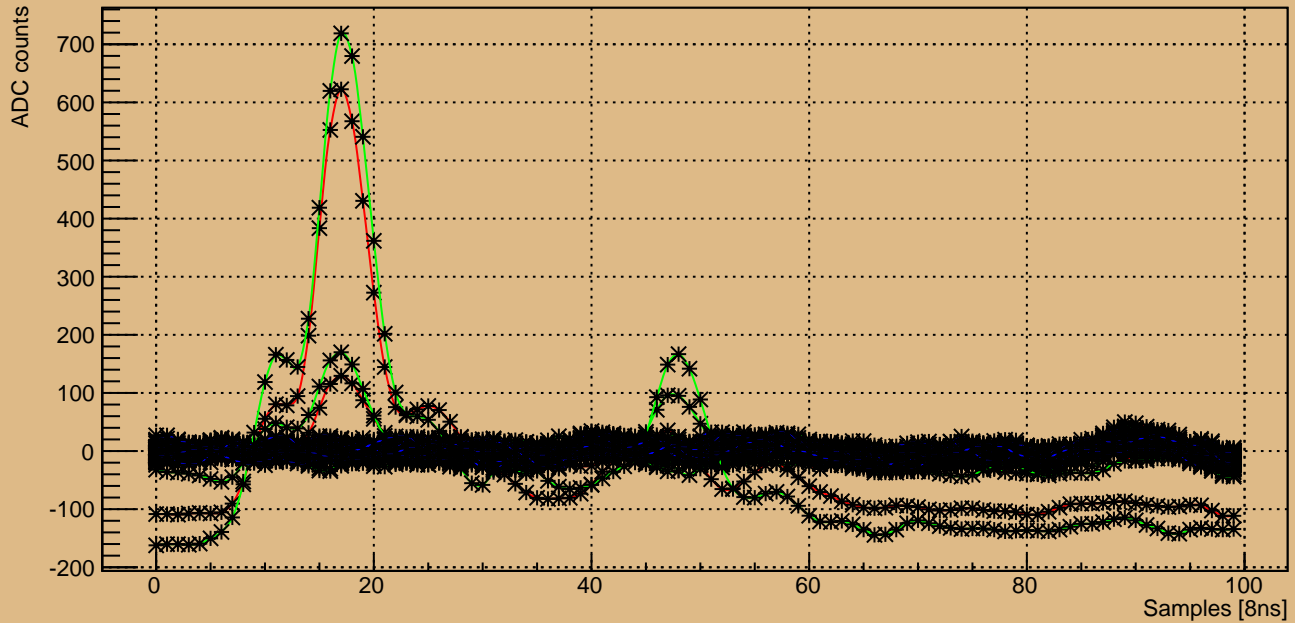
ADC125 Crate 1 ADC 1 raw data 72 channels



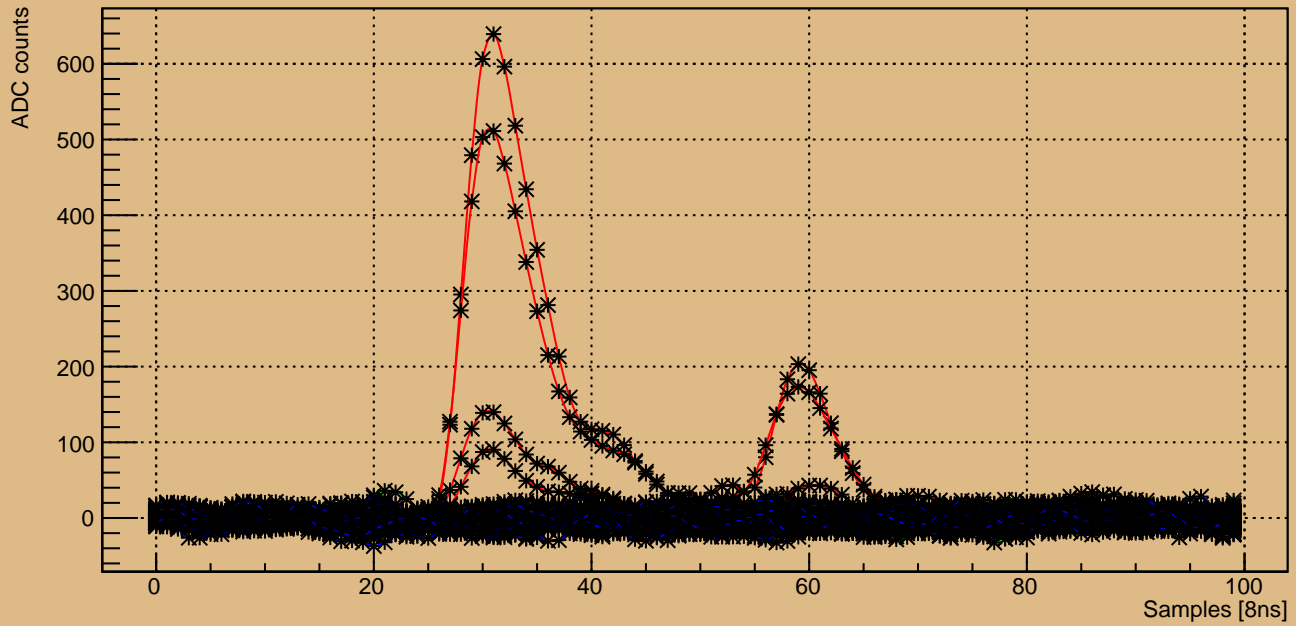
ADC125 Crate 2 ADC 0 raw data 72 channels



ADC125 Crate 1 ADC 1 raw data 72 channels



ADC125 Crate 2 ADC 0 raw data 72 channels



ADC125 Crate 1 ADC 1 raw data 72 channels

