

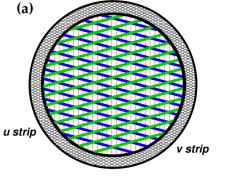
FDC Performance Plugin

Alexander Austregesilo

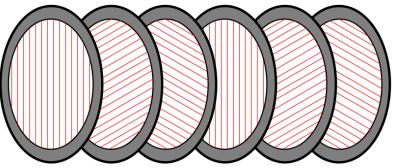




Detector Overview



Cell: Wires + 2 Cathode Layers



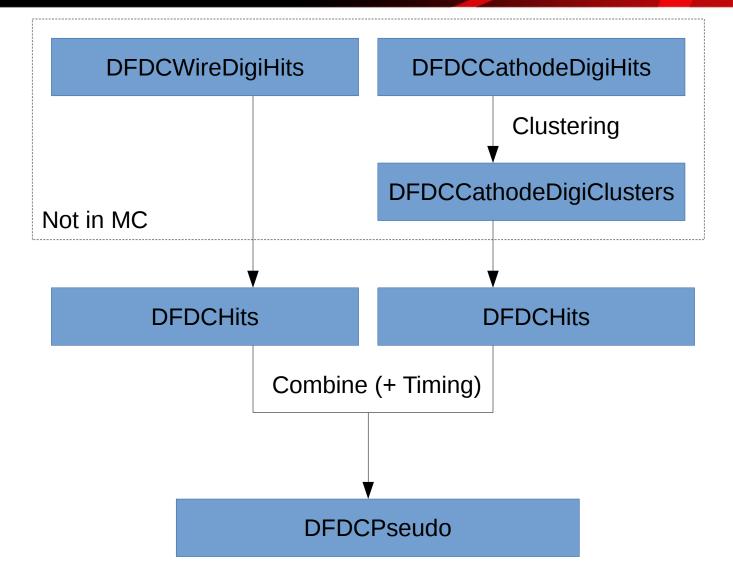
Package: 6 Cells

FDC: 4 Packages = 24 Cells





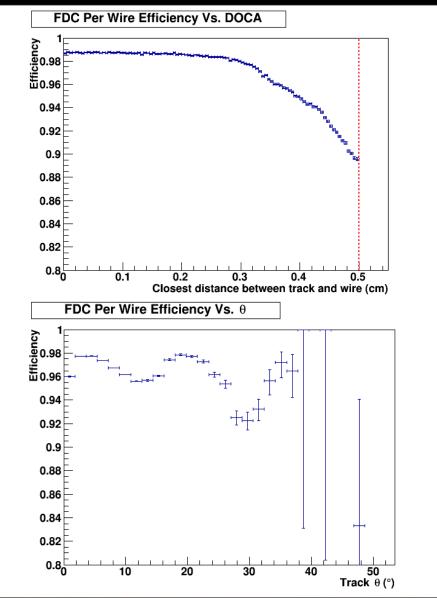
Reconstruction Overview



Jefferson Lab



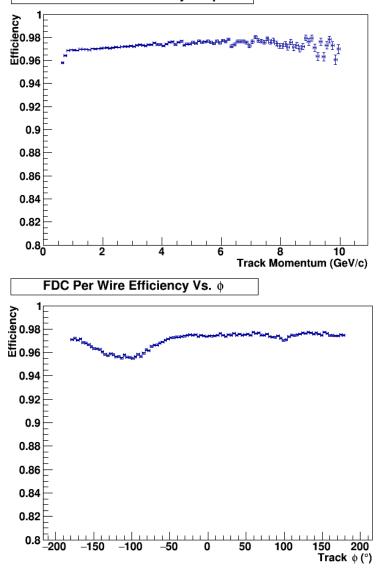
(DFDCHit) Wire Efficiency



U.S. DEPARTMENT OF

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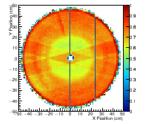
FDC Per Wire Efficiency Vs. p

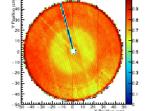


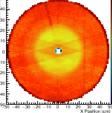


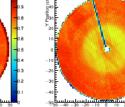


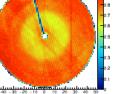
(DFDCPseudo) Efficiency

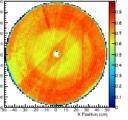


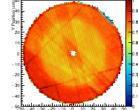


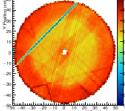


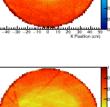


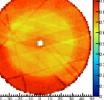


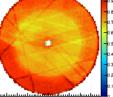


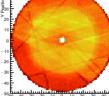


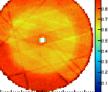


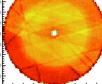


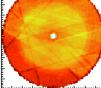


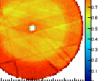


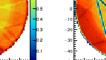


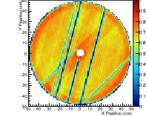


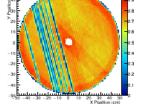


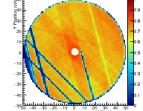


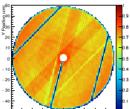


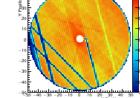


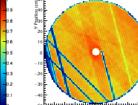


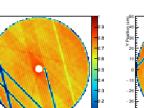








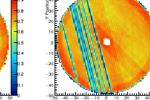


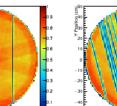


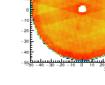


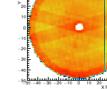
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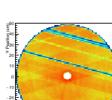


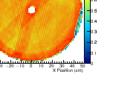


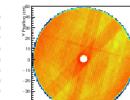


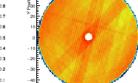




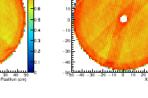


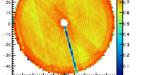


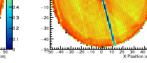


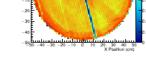


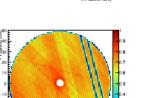






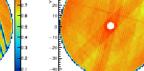




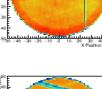


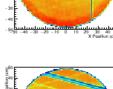






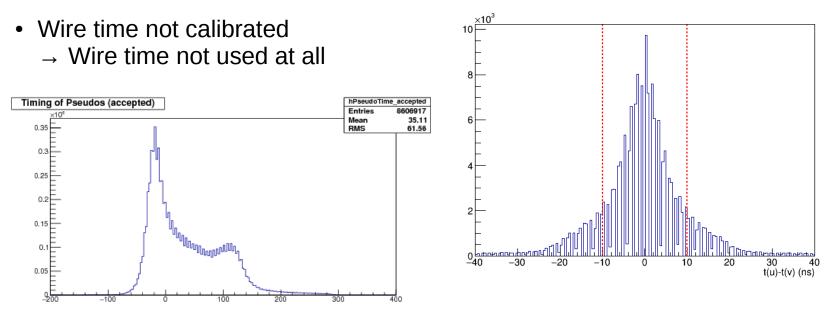




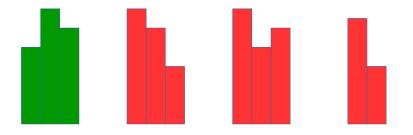


Pseudo Hit Factory

Timing Cut between U and V strips too narrow



Only certain types of strip clusters

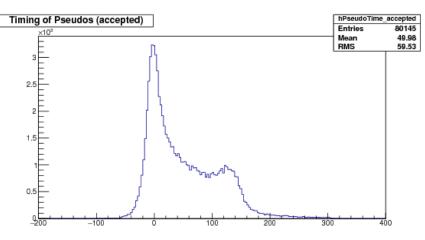


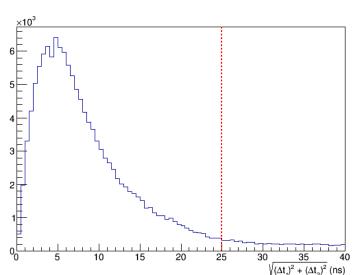




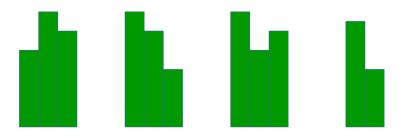
Pseudo Hit Factory

- Timing Cut between U and V strips too narrow \checkmark
- Wire time not calibrated ✓ (almost)
 → Wire time not used at all ✓





• Only certain types of strip clusters \checkmark

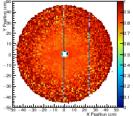


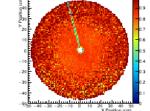
Efficiency 69.1% -> 84.3% Increase of # of Pseudos: 16%

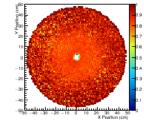


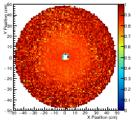


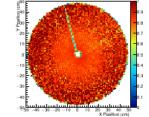
(DFDCPseudo) Efficiency

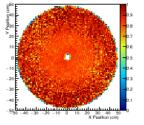


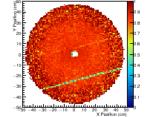


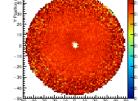


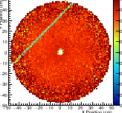




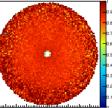


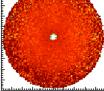


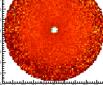






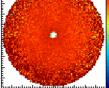


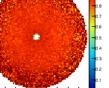


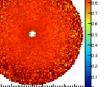


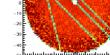


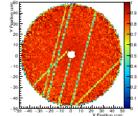


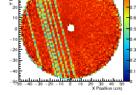


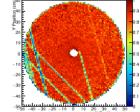


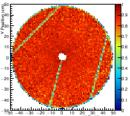


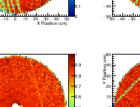


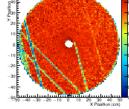


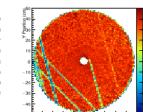


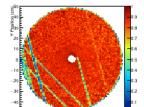


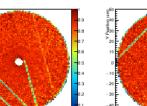














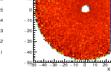
X Position (cm

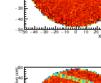
(D)

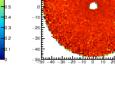


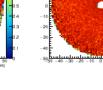
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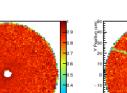














Summary

Conclusion:

- Performance study included in REST production
- Histograms used to calibrate timing of cathodes and wires
- Improved Pseudo_factory
 => Higher and more uniform Efficiency

Next steps:

- Timing info in tracking (done?)
- Comparison with MC
- Run-dependent performance study



