

HP generator

500 Om / 1.5 k

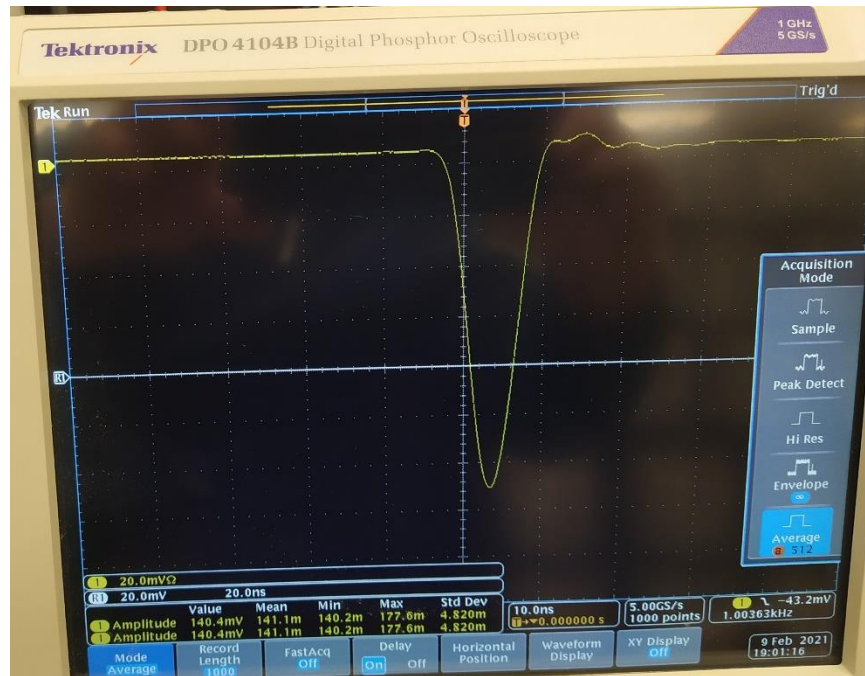
50 Om

Scope

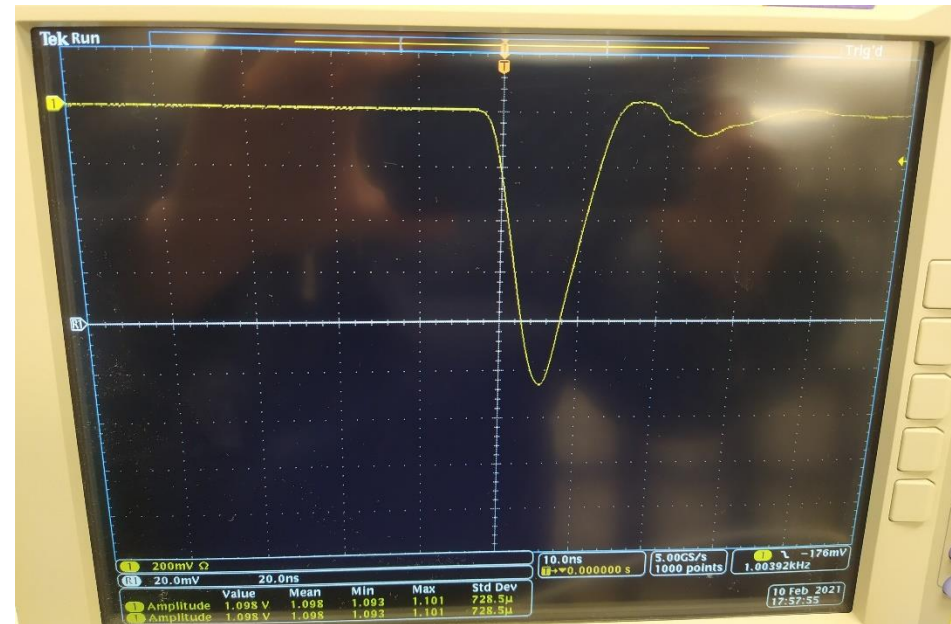
Amplifier gain ~3

<p>CONFIDENTIAL INFORMATION. The information in this document shall not be disclosed, copied nor disseminated unless authorized by the authors.</p>		<p>Jefferson Lab</p>	
<p>Drawn: CSB</p>	<p>Date: 9/4/2020</p>	<p>Title: Hall D Electronics ConCal 5_16_2017</p>	
<p>Checked:</p>	<p>Date:</p>	<p>Approved:</p>	<p>Revision:</p>
<p>Engineer:</p>	<p>File:</p>	<p>Drawing Number:</p>	<p>Sheet of *</p>

Signal pulse from generator

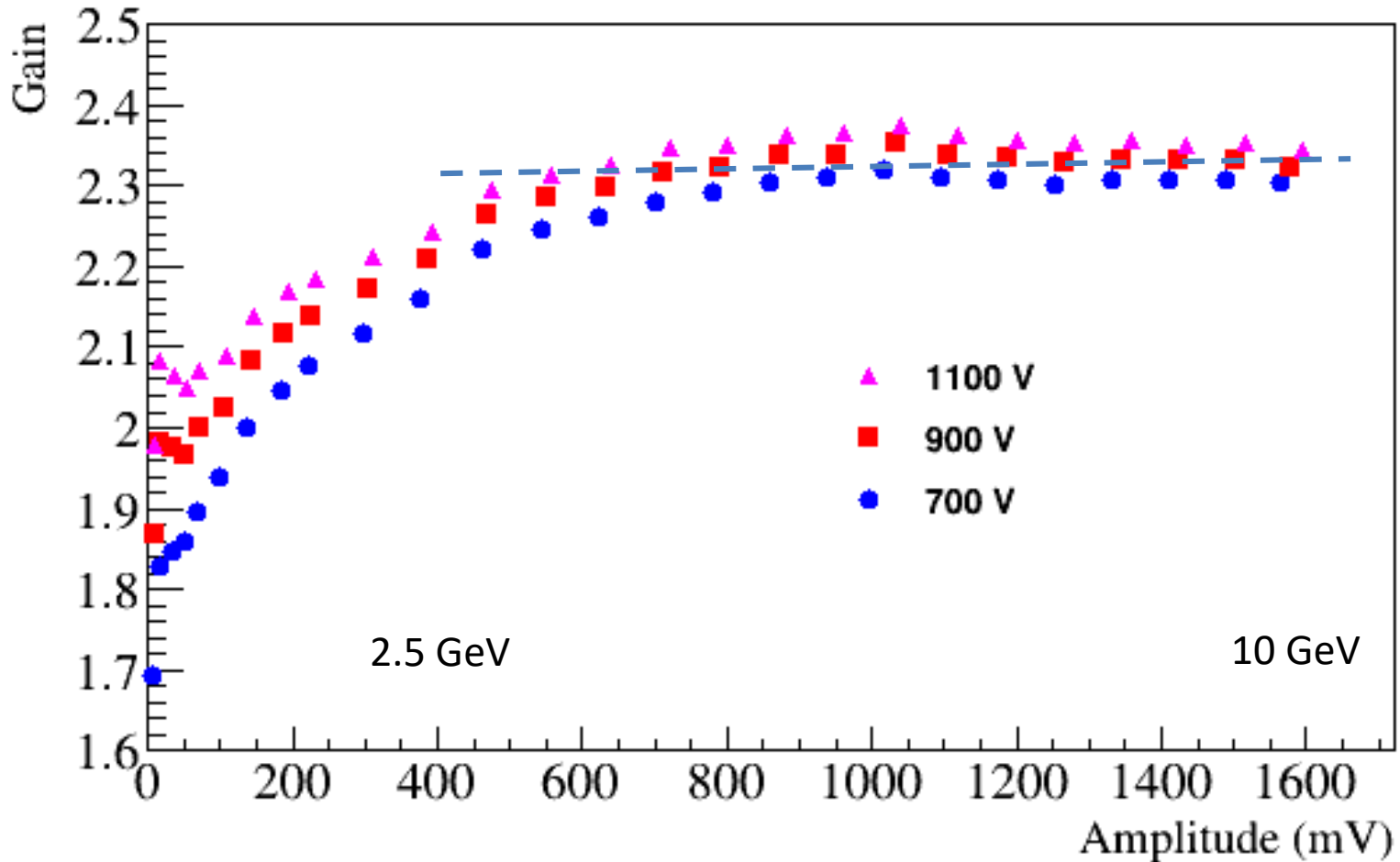


Amplified signal pulse



full width ~10 ns

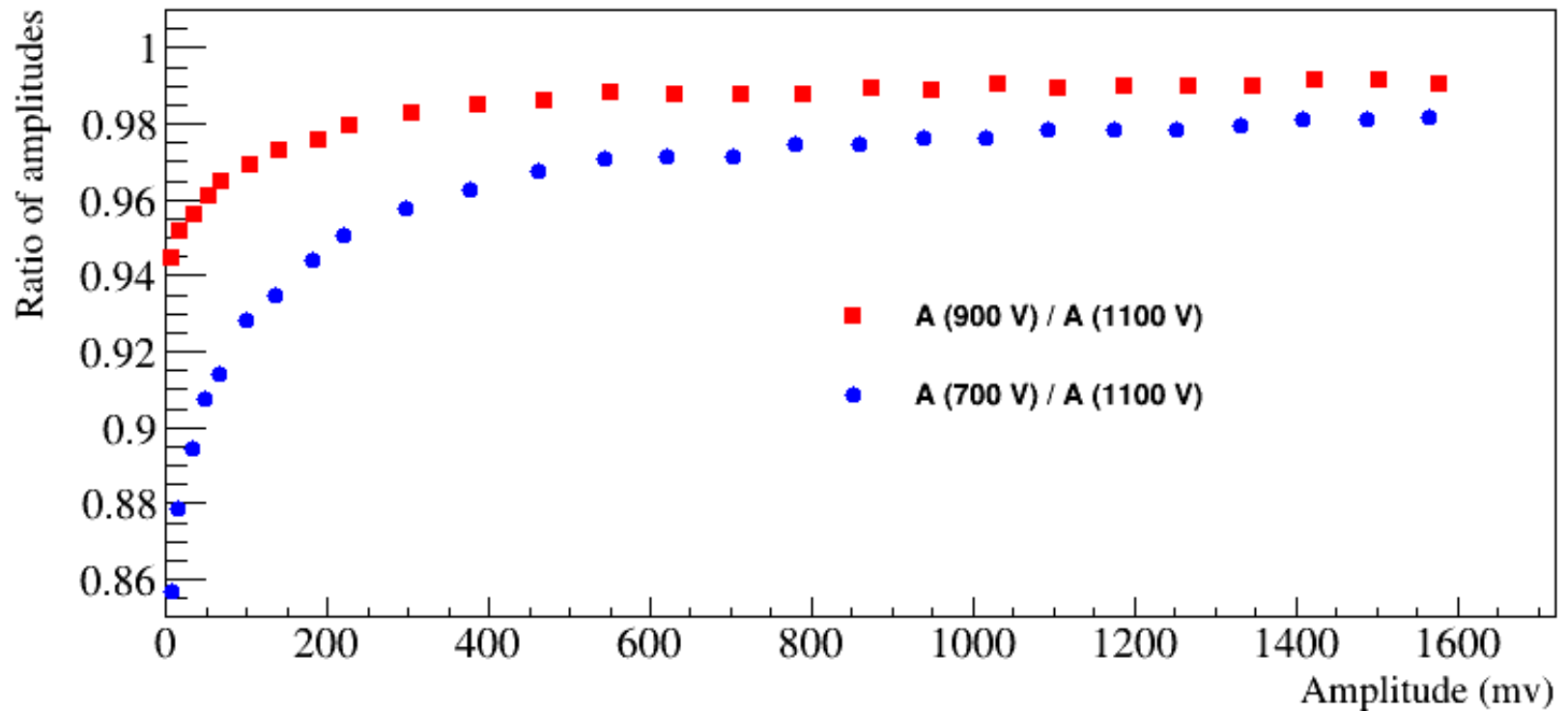
Amplifier Gain ($A_{\text{amp}} / A_{\text{HP}}$)



- Relatively stable gain for amplitudes between 0.5 V and 1.6 V
- Non linearity on the level of 10 % below 0.5 V

Non linearity for different divider current

700 V - 700 μ A
900 V - 900 μ A
1100 V - 1.1 mA



- Better linearity at larger divider currents

Discussion

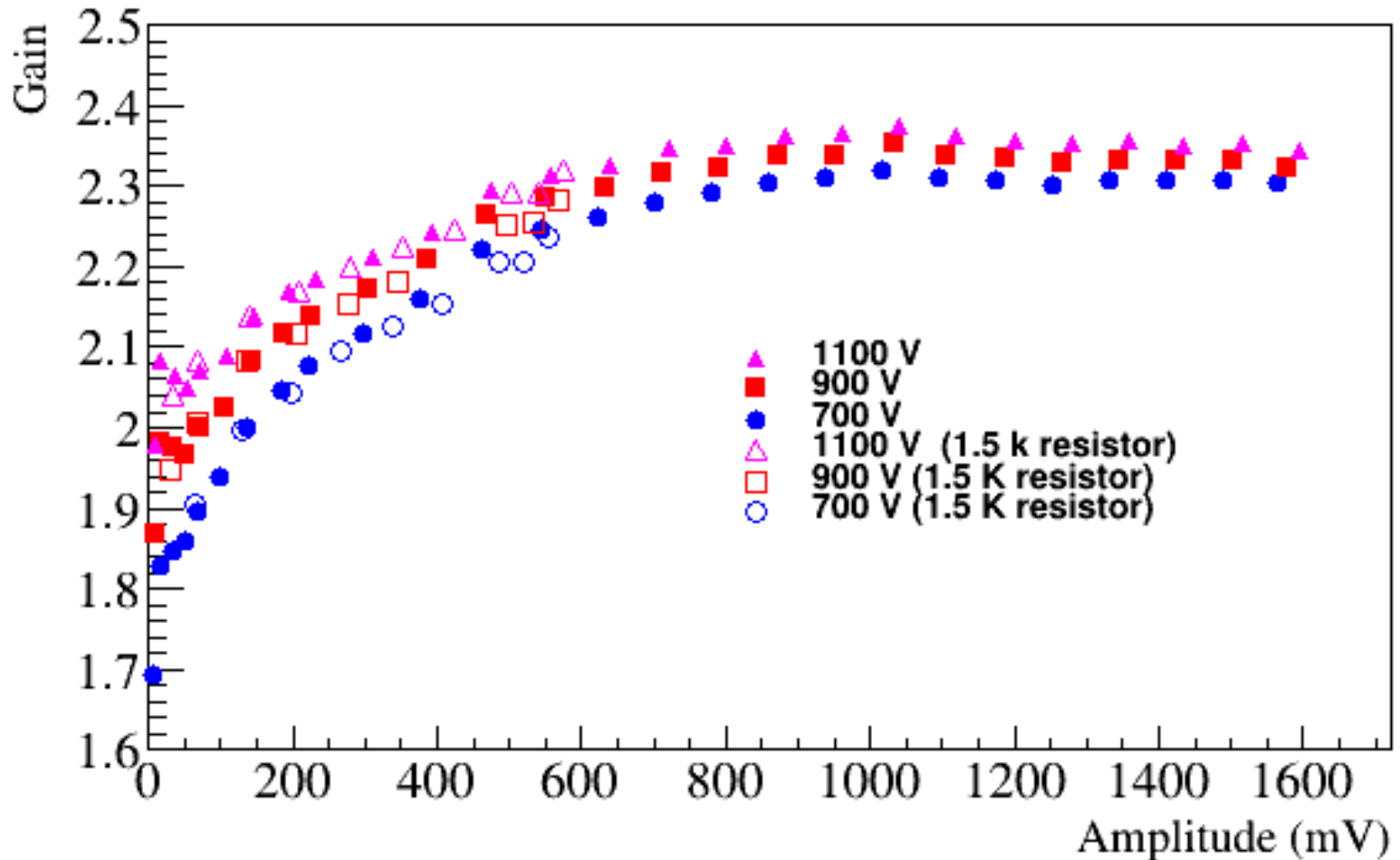
Measured gain verified our beam tests results
(if measured the gain correctly . . .)

FCAL:

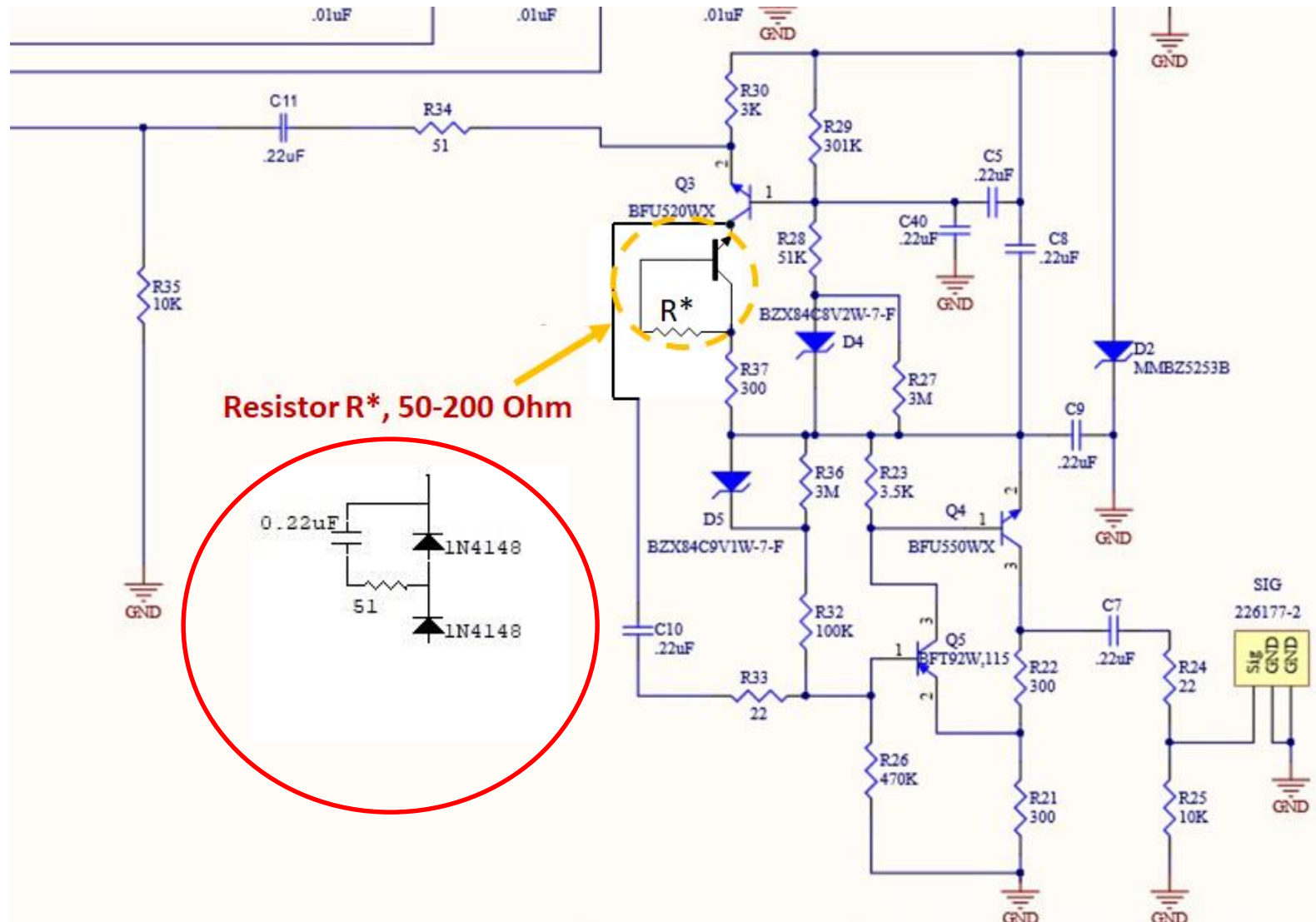
- An amplifier will be needed for inner FCAL insert layers, though with a relatively small gain between 3 and 6.
- Possible solutions to improve the amplifier:
 - use on-board amplifier, provide additional power to the amplifier (decouple power for divider and amplifier), use one extra cable to each PCB
 - use external amplifiers for modules in inner layers (place inside dark room ?)
 - apply non-linearity corrections for already existing bases

Amplifier Gain (A_{amp} / A_{HP})

Different 'injection' resistor

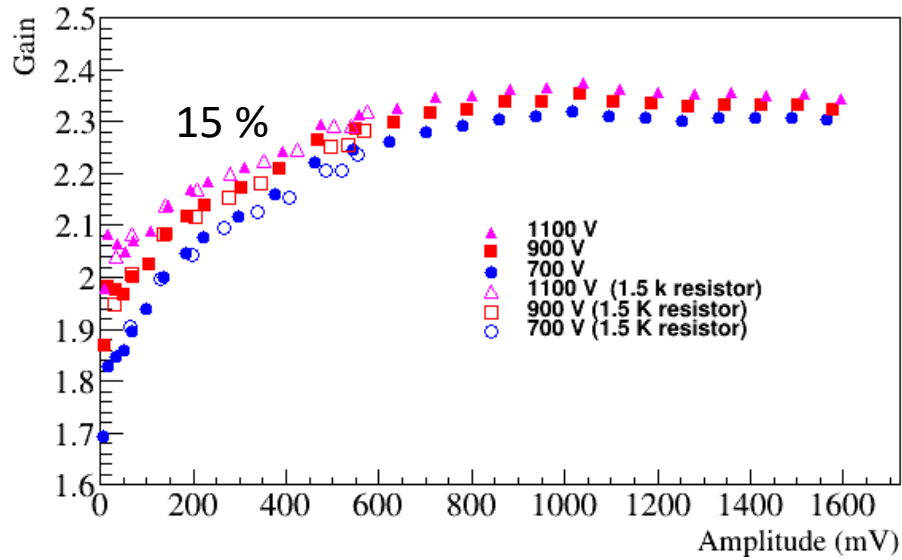


Modifications of the base

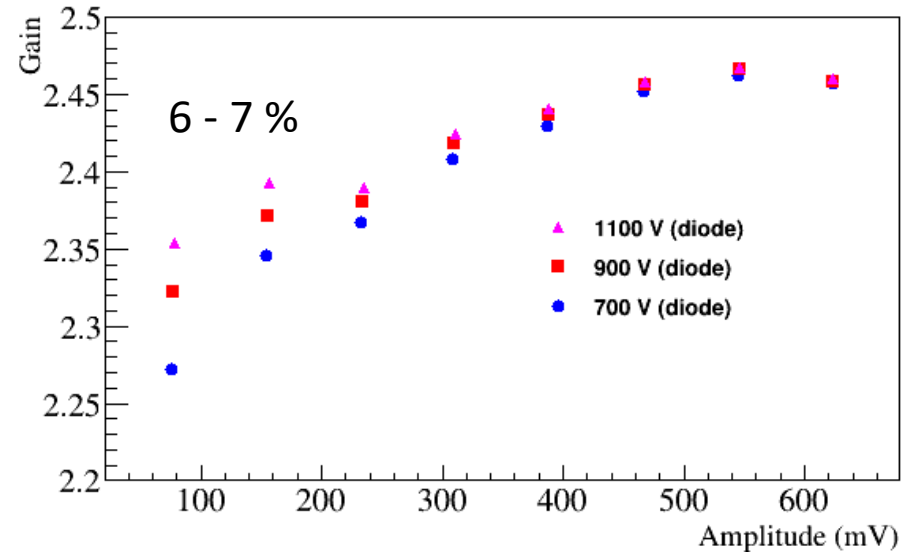


Amplifier Gain (A_{amp} / A_{HP})

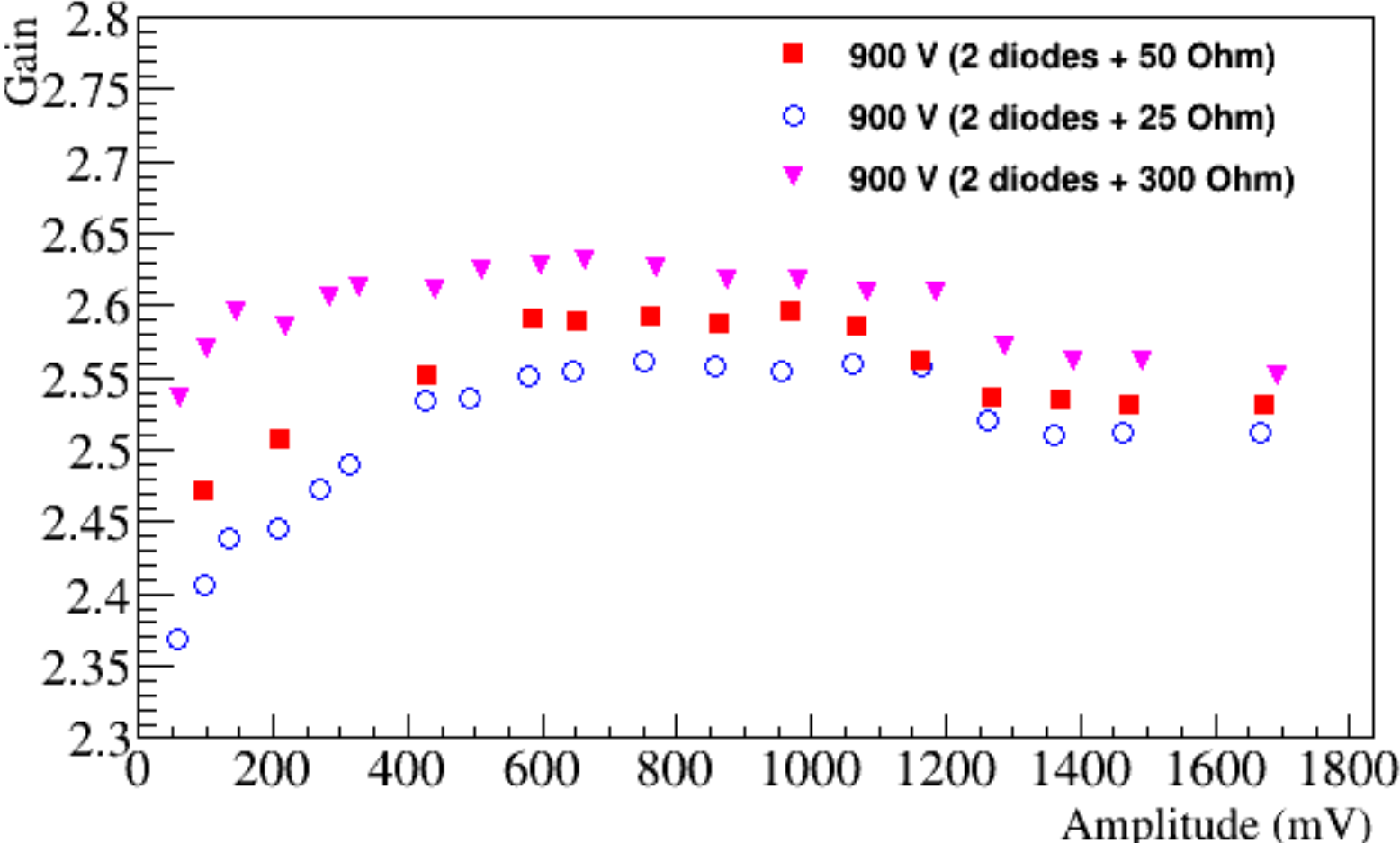
Default amplifier



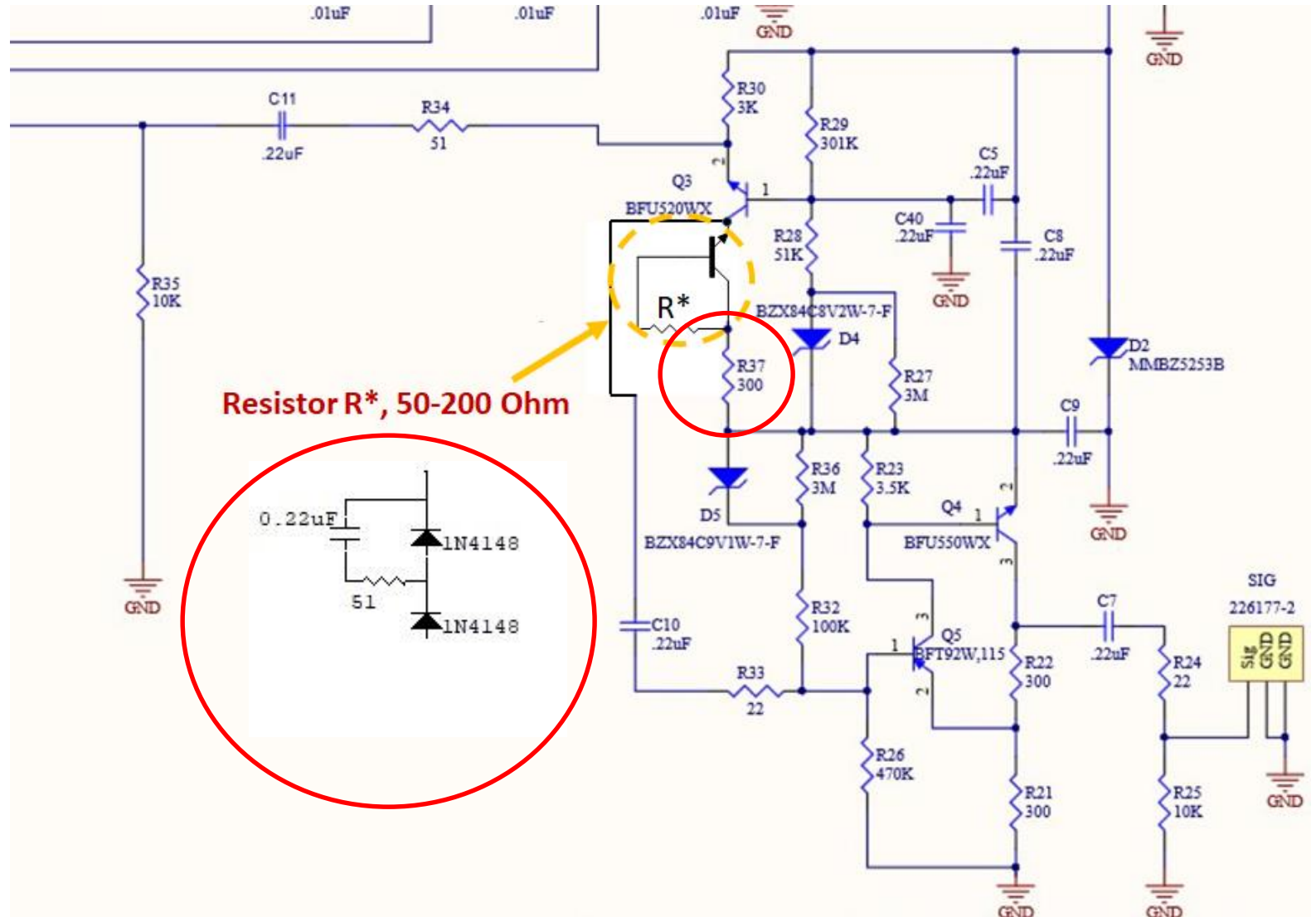
Add diode



Two diodes



Nex Steps



Discussion

Test 1 – 2 options

Start redoing dividers for CCAL.

Default: bypassed amplifier

Optional: gain of 3, the best we can get
no space on the existing PCB for 2-diode scheme

Continue with the divider optimization when the lab is re-opened

Consider on-board OPS bases amplifier (Fernando's design)
- perform tests in the lab

Prepare new dividers and install them after the PrimEx run
- test with the SRC experiment during this year