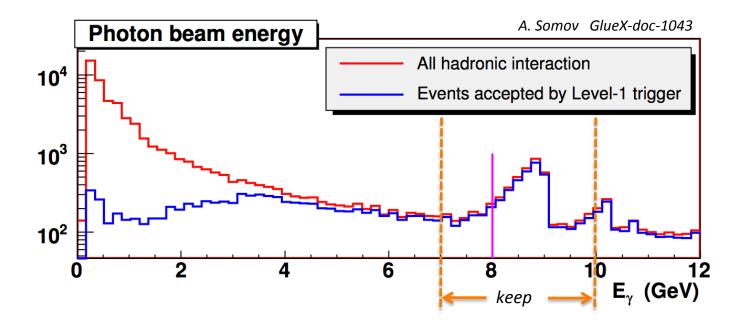
## Level-3 Trigger Farm CPU Requirements

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## L3 trigger goal

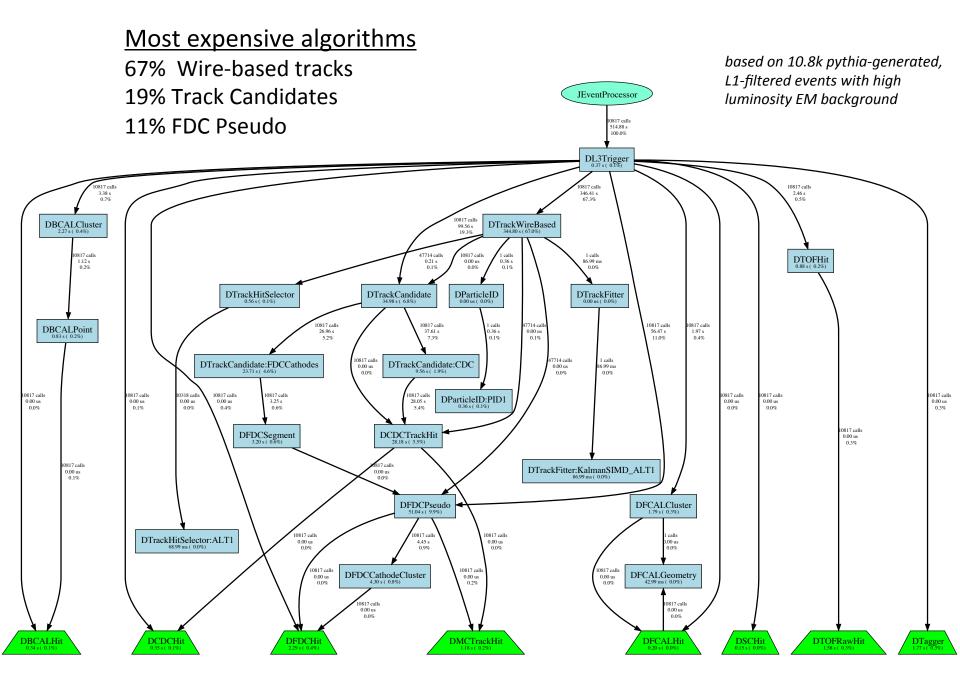
- Remove ~90% of events that pass Level-1 trigger (possible?)
  - Cut events with energies less than ~7GeV or greater than ~10GeV



## Method

- ◆ List below provides inputs that could be used to determine the accept/reject state of the L3 trigger:
  - Definitely accept
  - Definitely reject
  - Default accept
- Some values take much more CPU to obtain
  - Quick decisions will be tested first and expensive ones only if they fail to provide a definitive answer
- ◆ For current study, all values are calculated indicating worst-case scenario for CPU requirement

```
// Add data members here. For example:
                          // Number of reconstructed tagger hits
  int Ntagger;
  int Nstart counter;
                          // Number of start counter hits
                          // Number of TOF hits
  int Ntof;
  int Ncdc_layers;
                          // Number of different CDC layers hit
  int Nfdc_planes;
                          // Number of different FDC planes hit
                          // Number of FDC hits (cathode + anode)
  int Nfdc;
  int Nfdc_pseudo;
                          // Number of FDC pseudo hits
                          // Number of CDC hits
  int Ncdc:
  int Ntrack_candidates;
                          // Number of track candidates
  int Ntrack_wb;
                          // Number of wire-based tracks
float Ptot_tracks_wb;
                          // Scaler sum of total momentum from wire-based tracks
  int Nbcal_clusters;
                          // Number of BCAL clusters
                          // Number of FCAL clusters
  int Nfcal_clusters;
float Ebcal;
                          // Total energy in BCAL (rough estimate)
float Efcal;
                          // Total energy in FCAL
                          // true if event passes L3 trigger
 bool L3good;
```



## Results Summary

- Single core processing rate: 22Hz per core
  - (106Hz per 5cores)
- Without wire-based tracking rate is 3x higher
- To handle 20kHz low-luminosity trigger rate we would need ~910 cores ( 20kHz/22Hz)
  - 29 boxes with 32 coresor
  - 15 boxes with 64 cores
- Without wire-based tracking we would need only ~303 cores
  - 10 boxes with 32 cores or
  - 5 boxes with 64 cores
- Project has \$39k for L3 farm equipment infrastructure